

Electronic Acknowledgement Receipt

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Application Number:	10695163
International Application Number:	
Confirmation Number:	7172
Title of Invention:	CONFINED SPACERS FOR DOUBLE GATE TRANSISTOR SEMICONDUCTOR FABRICATION PROCESS
First Named Inventor/Applicant Name:	Leo Mathew
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Filer:	Michael J. Balconi-Lamica/Pat Thomas
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for Certificate of Correction	SC12746TP-USA-20080211-Request-Certificate-of-Correction.pdf	43149 587698b6c0d9a9491b304d5b1982b67b5ee7356	no	2

Warnings:

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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